


Substitute for Form 1449/PTO INFORMATION DISCLOSURE STATEMENT BY APPLICANT <i>(use as many sheets as necessary)</i>				Complete If Known			
				Application Number	10/748,545		
				Filing Date	December 29, 2003		
				First Named Inventor:	Mark L. Doczy et al.		
				Art Unit	2812		
				Examiner Name	Unassigned		
				Attorney Docket Number	42P18245		
Sheet	1	of	1				
U.S. PATENT DOCUMENTS							
Examiner Initials*	Cite No. ¹	Document Number		Publication Date MM-DD-YYYY	Name of Patentee or Applicant of Cited Document	Pages, Columns, Lines, Where Relevant Passages or Relevant Figures Appear	
		Number-Kind Code ² (if known)					
TN	OIPF APR 08 2005 PATENT & TRADEMARK	US-	5,879,998	03-1999	Krivokapic		
		US-	6,074,921	06-2000	Lin		
		US-	6,365,474 B1	04-2002	Finder et al.		
		US-	6,579,784 B1	06-2003	Huang		
		US-	2002/0024119 A1	02-2002	Tanaka et al.		
		US-	2002/0063299 A1	05-2002	Kamata et al.		
		US-	2003/0073273 A1	04-2003	Azuma et al.		
		US-	2003/0107088 A1	05-2003	Inumiya et al.		
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FOREIGN PATENT DOCUMENTS							
Examiner Initials*	Cite No. ¹	Foreign Patent Document		Publication Date MM-DD-YYYY	Name of Patentee or Applicant of Cited Document	Pages, Columns, Lines, Where Relevant Passages or Relevant Figures Appear	T ⁶
		Country Code ³	Number ⁴ Kind-Code ⁵ (if known)				

Examiner Signature: 	Date Considered: 11/9/05
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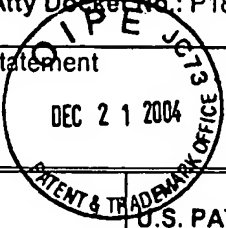
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Form PTO-1449 (Modified)		Atty Docket No.: P18245		Serial No.: 10/748,545	
List of Patents and Publications Statement (Use several sheets if necessary)				Applicant: Mark L. Doczy et al.	
				Filing Date: December 29, 2003	



REFERENCE DESIGNATION						
Examiner Initials	Document No.	Class	Sub-Class	Filing date if appropriate		
<i>Tu</i> <i>m</i>	AA	6,642,131 B2	Harada	438	591	<div style="border-left: 1px solid black; height: 100%; width: 100%;"></div>
	AB	6,667,246 B2	Mitsubishi et al.	438	756	
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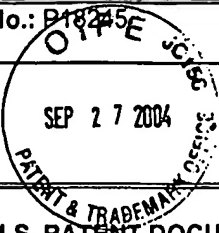
FOREIGN PATENT DOCUMENTS							
	Document No.	Date	Country	Class	Sub-Class	Translation	
AP							<div style="border-left: 1px solid black; height: 100%; width: 100%;"></div>
AQ							
AR							

OTHER ART (Including Author, Title, Date, Pertinent Pages, etc.)		
	AS	
	AT	
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	AV	
	AW	
	AX	
	AY	
	AZ	

Examiner <i>Heam</i>	Date Considered <i>11/9/05</i>
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REFERENCE DESIGNATION			U.S. PATENT DOCUMENTS			
Examiner Initials	Document No.	Class	Sub-Class	Filing date if appropriate		
TW	AA	6,255,698 B1	Gardner et al.	257	369	
	AB					
	AC					
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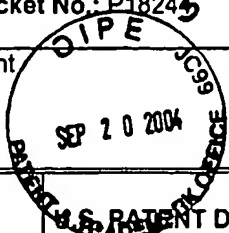
FOREIGN PATENT DOCUMENTS							
Document No.	Date	Country	Class	Sub-Class	Translation		
AP	EP 0 899 784 AZ	3/3/1999					
AQ	GB 2 358 737 A	4/24/2001					
AR							

OTHER ART (Including Author, Title, Date, Pertinent Pages, etc.)	
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Examiner: <i>Heath</i>	Date Considered: <i>11/9/05</i>
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REFERENCE DESIGNATION

Examiner Initials		Document No.		Class	Sub-Class	Filing date if appropriate
TN	AA	6,410,376 B1	Ng et al.	438	199	
TN	AB	6,586,288 B2	Kim et al.	438	183	
TN	AC	US2002/0058374 A1	Kim et al.	438	228	
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AP						
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OTHER ART (Including Author, Title, Date, Pertinent Pages, etc.)

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Examiner *Ullrich* Date Considered

11/9/05

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		Number-Kind Code ² (if known)				
		US-	6,365,450 B1	4/2/2002	Kim	
		US-				
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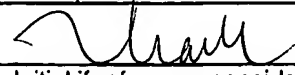
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Form PTO-1449 (Modified)		Atty Docket No.: 42P18245		Serial No.: Unknown 10/748545	
List of Patents and Publications Statement (Use several sheets if necessary)				Applicant: Mark L. Doczy et al.	
				Filing Date: Herewith 12/29/03	

REFERENCE DESIGNATION			U.S. PATENT DOCUMENTS			
Examiner Initials		Document No.		Class	Sub-Class	Filing date if appropriate
TW	AA	6,063,698	Tseng et al.	438	585	
	AB	6,184,072	Kaushik et al.	438	197	
	AC	6,420,279	Ono et al.	438	785	
	AD	6,475,874	Xiang et al.	438	396	
	AE	6,514,828	Ahn et al.	438	240	
	AF	6,544,906	Rotondaro et al.	438	785	
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	AH	6,617,210	Chau et al.	438	240	
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	AJ	US2003/032303	Yu et al.	438	770	
	TW	AK	US2003/0045080	Visokay et al.	438	591

FOREIGN PATENT DOCUMENTS							
		Document No.	Date	Country	Class	Sub-Class	Translation
	AL						
	AM						

OTHER ART (Including Author, Title, Date, Pertinent Pages, etc.)			
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	AO	Doug Barlage et al., "High-Frequency Response of 100nm Integrated CMOS Transistors with High-K Gate Dielectrics", 2001 IEEE, 4 pages.	
	AP	Lu et al., "Dual-Metal Gate Technology for Deep-Submicron CMOS Devices", dated April 29, 2003, 1 page.	
	AQ	Schwantes et al., "Performance Improvement of Metal Gate CMOS Technologies with Gigabit Feature Sizes", Technical University of Hamburg-Harburg, 5 pages.	
	AR	Doczy et al., "Integrating N-type and P-type Metal Gate Transistors", Serial No. 10/327,293, Filed December 20, 2002	
	AS	Brask et al., "A Method of Making Semiconductor Device Having a High-K Gate Dielectric", Serial No. 10/387,303, Filed March 11, 2003	
	AT	Brask et al., "A Method of Making Semiconductor Device Having a High-K Gate Dielectric", Serial No. 10/391,816, Filed March 18, 2003	
	AU	Chau et al., "A Method for Making a Semiconductor Device Having a Metal Gate Electrode", Serial No. 10/431,166, Filed May 6, 2003	
	AV	Brask et al. "A Selective Etch Process for Making a Semiconductor Device Having a High-K Gate Dielectric," Serial No. 10/652,546, filed August 28, 2003	
	AW	Brask et al. "A Method for Making a Semiconductor Device Having a High-K Gate Dielectric," Serial No. 10/642,796, filed August 28, 2003	
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	AY	Brask et al., "A Method for Making a Semiconductor Device Having a Metal Gate Electrode", Serial No. 10/704,497, Filed November 6, 2003	
	AZ	Brask et al., "A Method for Etching a Thin Metal Layer", Serial No. 10/704,498, Filed November 6, 2003	
	TW	BA	Brask et al., "A Method for Making a Semiconductor Device that Includes a Metal Gate Electrode", Serial No. Unknown, Filed December 18, 2003

	BB	Brask et al. "A Method for Making a Semiconductor Device with a Metal Gate Electrode that is Formed on an Annealed High-K Gate Dielectric Layer", Serial No. Unknown, Filed December 19, 2003
Examiner		Date Considered 11/9/05
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